

TymeDeck™:

Transforms traditional IC R&D methodology to solve the challenges of time to market and engineering talent shortage. Empower engineering teams to focus on building and delivering highest quality IC chips on time.



SYNCAST

PRODUCT

TymeDeck™ is an automated software platform to strengthen the IC R&D methodology



Real-Time Insights Dashboard

Acquire an overview of the crucial design quality metrics and project status on a unified dashboard. Receive alert notifications for critical events (such as coverage drops, power and gate count violations, test failures, etc.) through both email and mobile devices. Give the earliest attention to critical issues.



Workflow Orchestration

Build the workflow through the user-friendly editor to increase control and flexibility in engineering methodology. Test early, catch bugs early, and deliver design early with full test coverage, including simulation, FPGA, power, performance, area, DFT, etc.



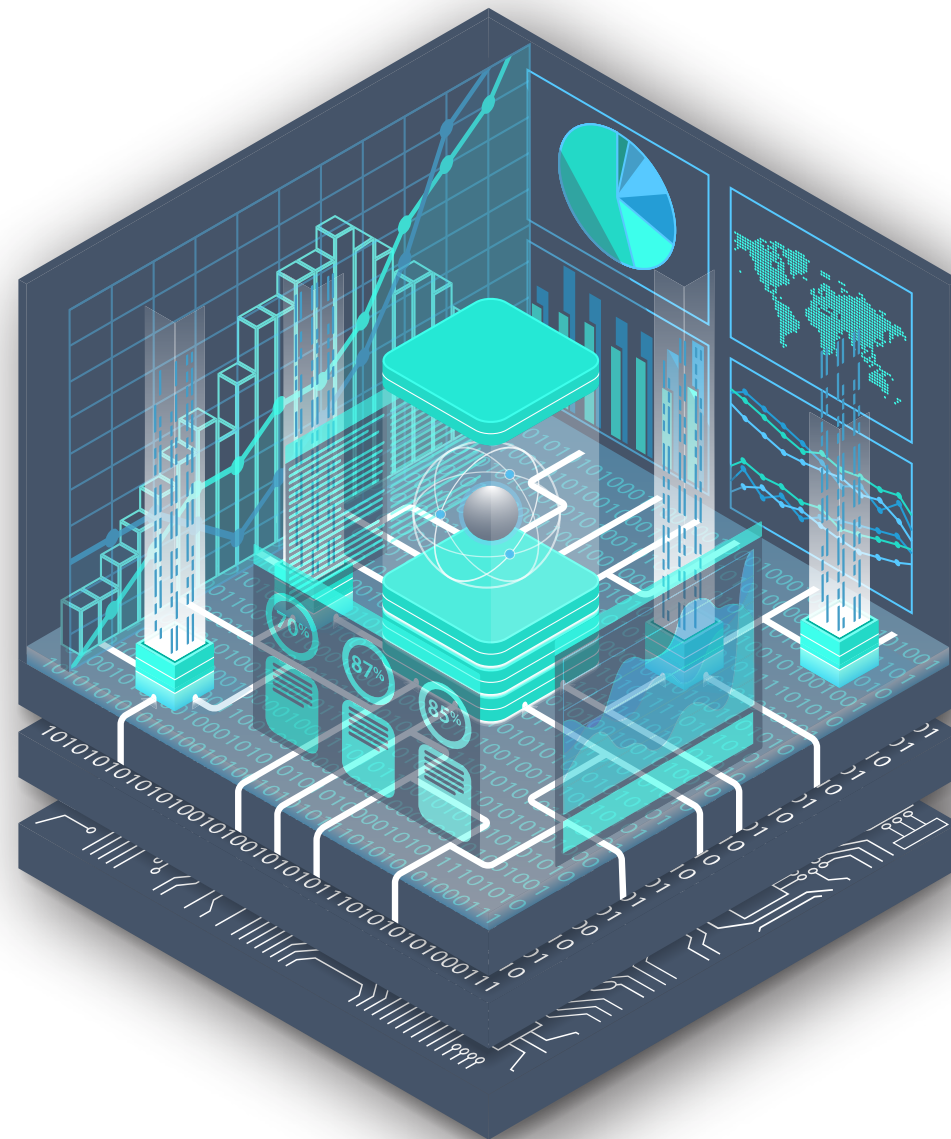
Intelligent Data Extraction

Setup active test log monitors that automatically detect patterns to extract insightful data. TymeDeck™ intelligently generates & updates JIRA and Bugzilla tickets upon test failure; an effective way to debug fast.



Automation Libraries

Share tasks and workflows through published libraries between team members. Maximize automation reusability and team collaboration. Easily add any EDA tool to a workflow with a minimal learning curve.



Test Flow Automation

- Simulation automation
- Fpga automation
- Emulation automation
- Static verification automation

Project Management Flow Automation

- Test status update automation
- Design metric update automation

Smart Data Extraction

- Extract transform load logs

Realtime Dashboard Creation

- Project status automation
- Test status automation
- Design metric automation

Resource Optimization

- Elastic compute
- License optimization
- Server optimization

Find Defects Here!



RTL Design



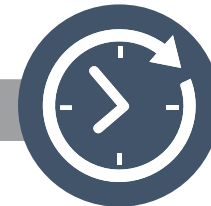
Functional Verification



Synthesis



Physical Design



Timing Analysis

Not Here!



Tape Out